

## Department of Electronics and Telecommunication Engineering

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HOD, E&TC

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### Vision Statement of E&TC Department :-

To emerge as a department of repute compatible to the changing needs of society at large.

### Mission Statement of E&TC Department :-

M1: To cultivate creativity and innovativeness in teaching learning process for developing competent professional.

M2: To equip graduating students with skill sets to meet current and future demands of industry, academia and society through well designed curriculum.

## HOD MESSAGE



**Dr. Yogesh S. Angal**  
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HOD (E&TC) JSPM BSIOTR Wagholi

E&TC Graduates find wide opportunities in today's growing communications era. Our mission is to cultivate creativity and innovativeness in teaching learning process for developing competent professionals also to equip graduating students with skill sets to meet current and future demands of industry, academia and society through well designed curriculum. The teaching module comprises lectures, tutorials and practical's. The methodology adopted in the classroom teaching is based on application of innovative strategies, comprehensive lectures, regular assignments and interaction between teachers and students.

## NEWSLETTER TEAM



**Prof. Meenakshi Annamalai**  
Chief Editor



**Kalyani Pawase**  
TE, E&TC



**Snehal Satav**  
TE, E&TC

### PROGRAM EDUCATIONAL OBJECTIVE(PEO's) of E&TC Department :-

PEO-1 :- Ability to pursue professional carrier.

PEO-2 :- An ability to adopt rapid changes in core & allied domain of Electronics and Telecommunication Engineering fulfilling industrial & societal needs.

PEO-3 :- An ability to deal with societal problems ethically delivering sustainable solution.

### PROGRAM SPECIFIC OUTCOMES (PSO's) of E&TC Department :-

PSO1 (Fundamental Knowledge and Analysis) :- An ability to understand, apply and analyze the engineering problems relevant to Electronics & Telecommunication Engineering.

PSO2 (System Design) :- Design the Electronics & Telecommunication Engineering systems using latest hardware and software tools by applying significant analytical knowledge.

PSO3 (Societal and Environmental Issues) :- An understanding societal, environmental, health, safety and cultural issues with ethical responsibility and function effectively as an individual and heterogeneous group.



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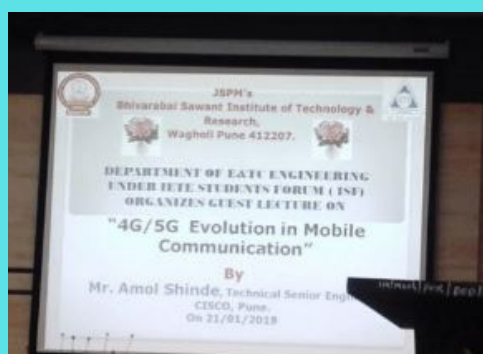
#### **Vision Statement of BSIOTR :-**

To provide, nurture and maintain an environment of high academic excellence, research and entrepreneurship for all aspiring students, which will prepare them to face global challenges maintaining high ethical and moral standards.

#### **Mission Statement of BSIOTR :-**

Satisfy the aspirations of youth force, who want to lead nation towards prosperity through techno-economic development.

## 4G/5G EVOLUTION IN MOBILE COMMUNICATION



The Department of Electronics and Telecommunication had organized a Guest Lecture on "4G/5G Evolution in Mobile Communication" under IETE Students Forum (ISF) for students. This lecture is organized to cover the basics and recent trends in Mobile Communication. It provides the advance knowledge required for mobile communication technology and development. Students are introduced to the concepts applicable to this technology particularly LTE. At the end of this lecture, the successful student will be able to: 1. Interconnect engineering concepts related to communication using wireless technology with real-world applications. Learn to employ specialized knowledge of subsystems 4G/5G. 2. Improve their capabilities of using the technical knowledge of recent trends like Long Term-Evolution (LTE) to design, test and verify different applications.

## A WEBINAR ON VLSI DESIGN USING VERILOG HDL

The Department of Electronics and Telecommunication had organized a Webinar "A Webinar On VLSI Design using Verilog HDL". Dr.Y.S.Angal, HOD, ENTC, BSIOTR had felicitated Mr. P. R. Sivakumar, CEO, Maven Silicon. Prof. V. G. Puranik had given vote of thanks. The session of the webinar was on "VLSI design & Designing of SOC" started at 10.00 am. Mr P R Sivakumar, CEO Maven Silicon, provided best knowledge regarding VLSI design and Designing of SOC. He provided valuable information of Verilog with its features, coding, applications & data types. He had given important information regarding how to debug the code, how to write the code using VHDL.

